

**DECLARATION (37 CFR 1.63) FOR UTILITY OR DESIGN APPLICATION USING AN
APPLICATION DATA SHEET (37 CFR 1.76)**

Title of Invention OVER-VOLTAGE PROTECTION OF INTEGRATED CIRCUIT I/O PINS

As the below named inventor(s), I/we declare that:

This declaration is directed to:

The attached application, or
 Application No. , filed on ,
 as amended on _____ (if applicable);

I/we believe that I/we am/are the original and first inventor(s) of the subject matter which is claimed and for which a patent is sought;

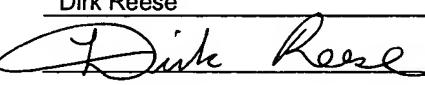
I/we have reviewed and understand the contents of the above-identified application, including the claims, as amended by any amendment specifically referred to above;

I/we acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me/us to be material to patentability as defined in 37 CFR 1.56, including for continuation-in-part applications, material information which became available between the filing date of the prior application and the national or PCT International filing date of the continuation-in-part application.

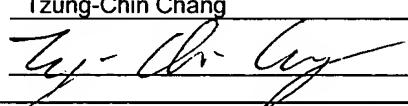
All statements made herein of my/our own knowledge are true, all statements made herein on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001, and may jeopardize the validity of the application or any patent issuing thereon.

FULL NAME OF INVENTOR(S)

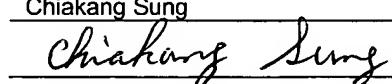
Inventor 1 Dirk Reese Date: 1/19/2004

Signature:  Citizen of: United States

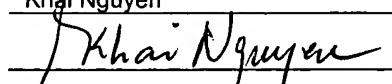
Inventor 2 Tzung-Chin Chang Date: 1/19/2004

Signature:  Citizen of: Taiwan

Inventor 3 Chiakang Sung Date: 1/20/2004

Signature:  Citizen of: United States

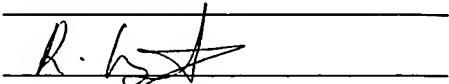
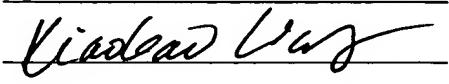
Inventor 4 Khai Nguyen Date: 1/20/2004

Signature:  Citizen of: United States

Additional inventors are being named on 1 additional form attached hereto.

**DECLARATION (37 CFR 1.63) FOR UTILITY OR DESIGN APPLICATION
USING AN APPLICATION DATA SHEET (37 CFR 1.76) –
ADDITIONAL INVENTOR(S)**
Supplemental Sheet
(Total of 3 forms are submitted.)

FULL NAME OF INVENTOR(S)

Inventor 5:	Gopinath Rangan	Date:	2/17/04
Signature:		Citizen of:	India
Inventor 6:	Xiaobao Wang	Date:	2/18/04
Signature:		Citizen of:	Peoples Republic of China
Inventor 7:		Date:	
Signature:		Citizen of:	
Inventor 8:		Date:	
Signature:		Citizen of:	

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